

INVENTOR

	Document ID	Title	Inventor
1	US 20040207879 A1	Systems and methods for error diffusion	Bailey, James Ray et al.
2	US 20040201642 A1	SYSTEMS AND METHODS FOR PRINthead ARCHITECTURE HARDWARE FORMATTING	Bates, John et al.
3	US 6801959 B1	Relaxed-timing universal serial bus with a start of frame packet generator	Crutchfield, David Allen et al.
4	US 6688590 B2	Dual tray printer with single drive shaft and dual media picks	Billings, Aaron Wendell et al.
5	US 6643728 B1	Method and apparatus for converting IEEE 1284 signals to or from IEEE 1394 signals	Crutchfield, David Allen et al.
6	US 20020196884 A1	Method and apparatus for effecting synchronous pulse generation for use in variable speed serial communications	Crutchfield, David Allen et al.
7	US 20020186392 A1	Quick edit and speed print capability for a stand-alone ink jet printer	Ahne, Adam Jude et al.
8	US 20020101024 A1	Dual tray printer with single drive shaft and dual media picks	Billings, Aaron Wendell et al.